

Figure 1

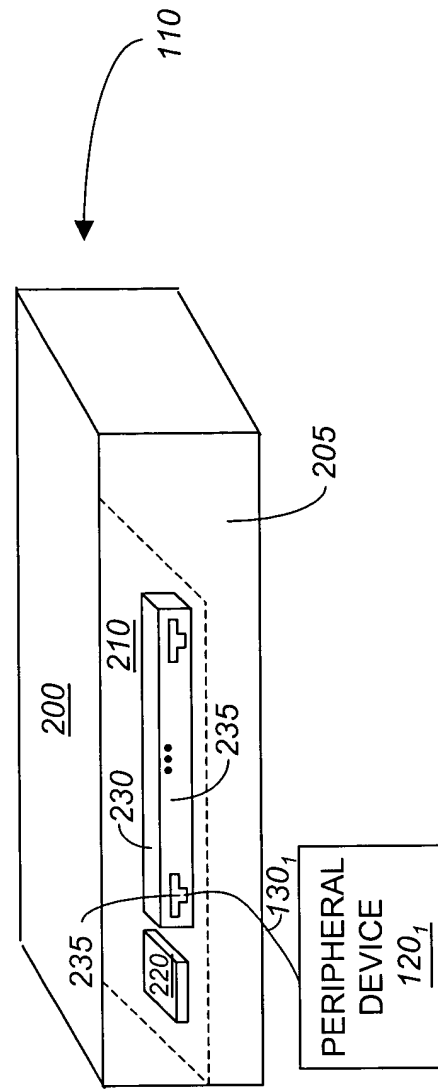


Figure 2

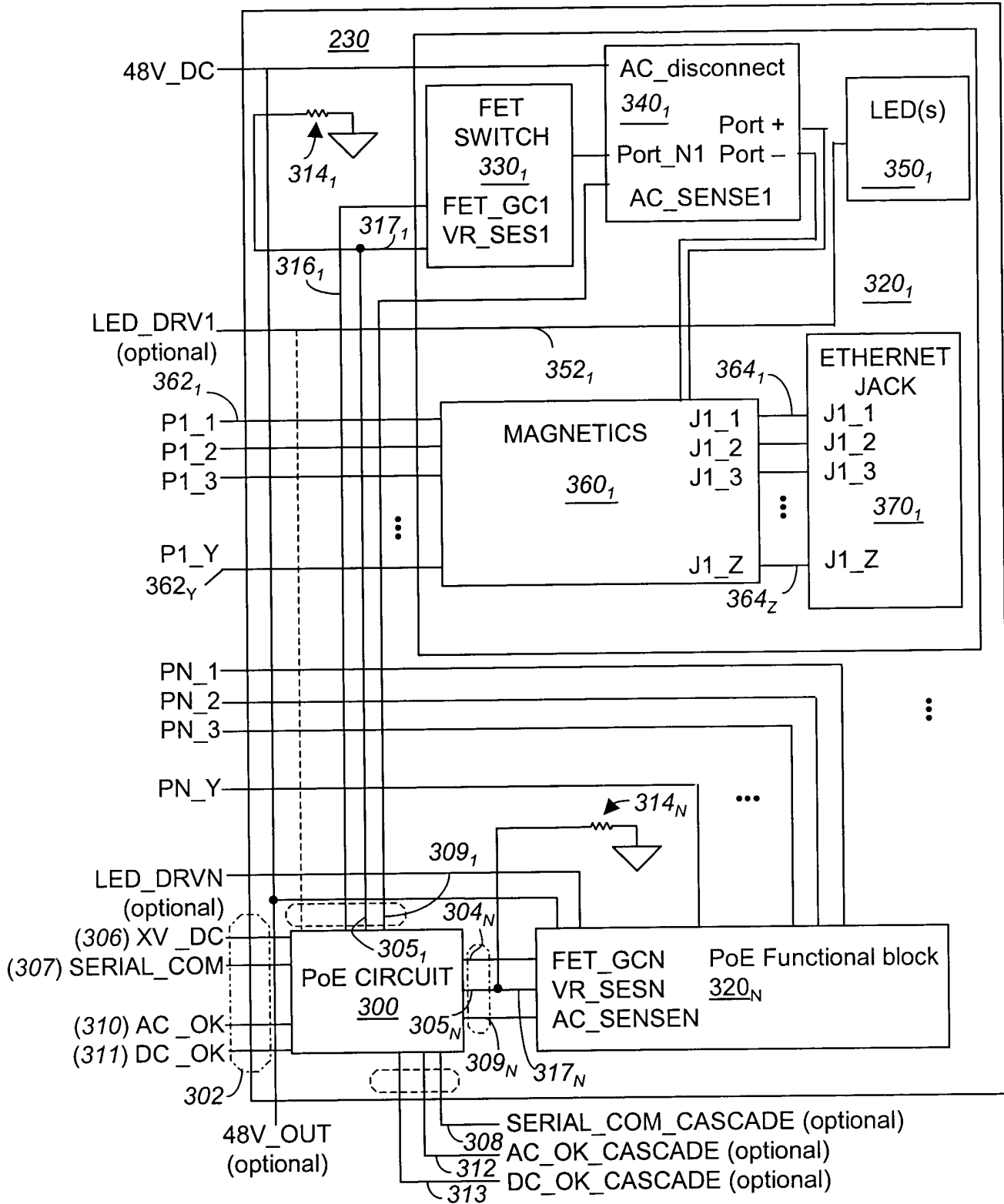


Figure 3

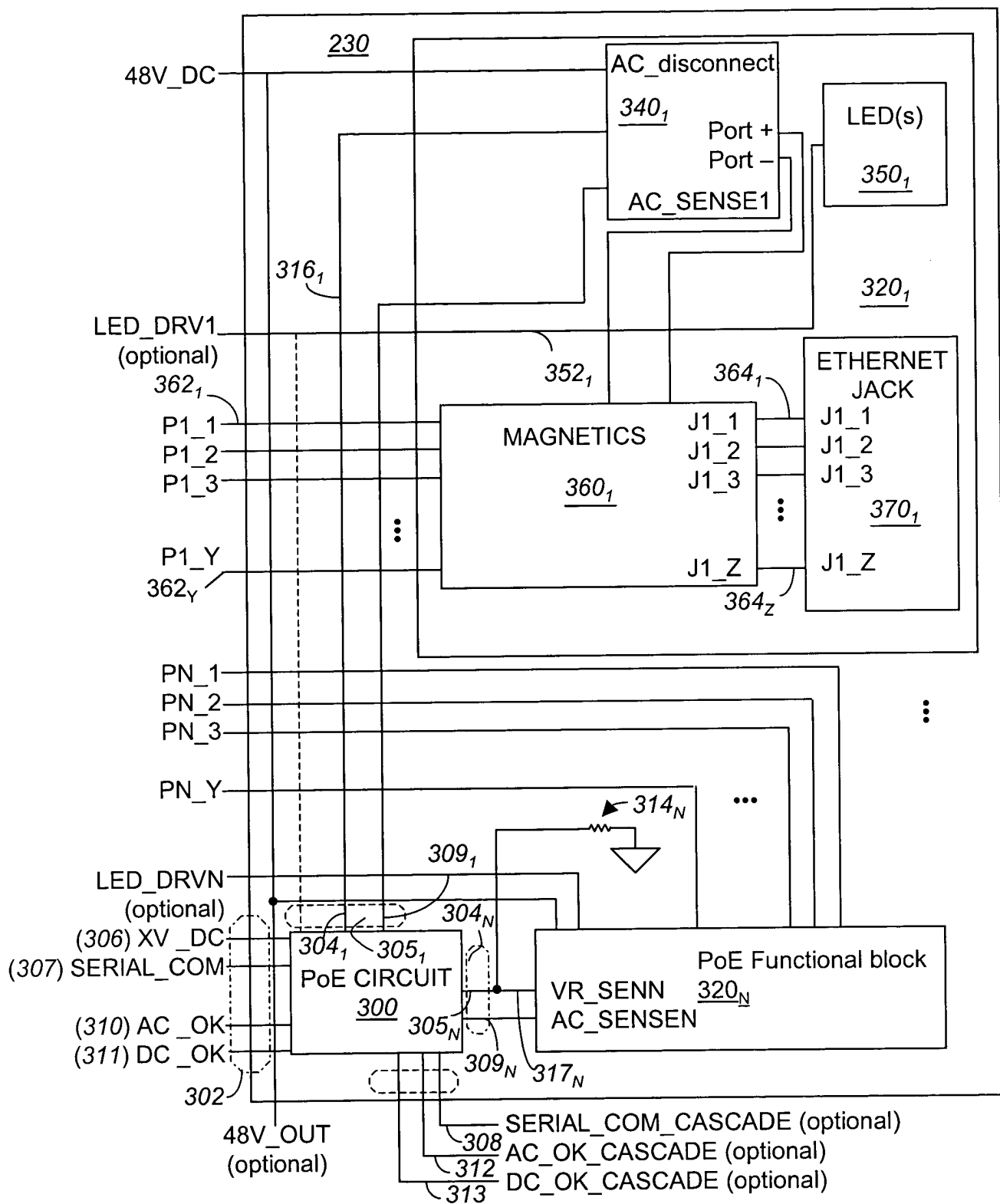


Figure 4

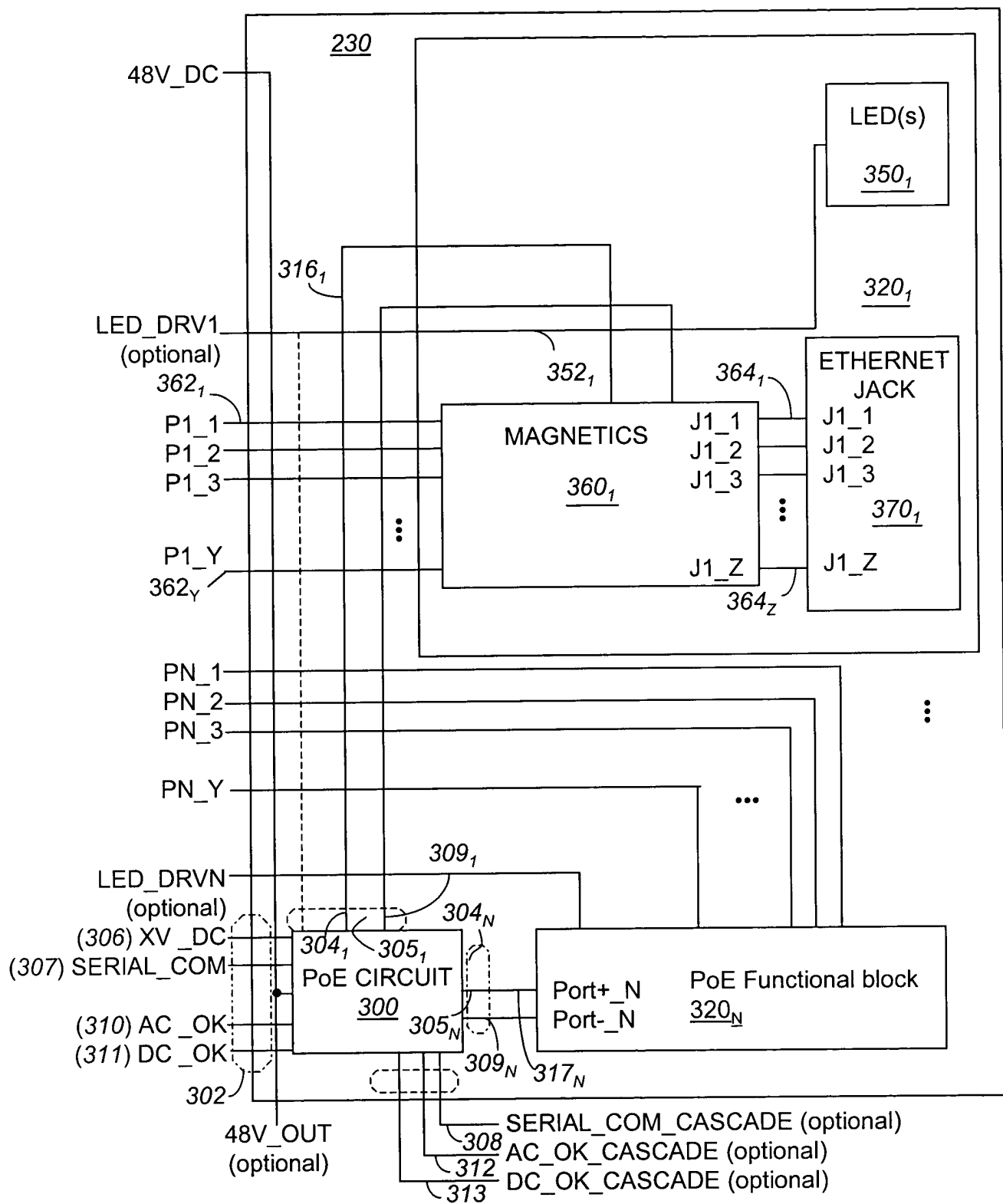


Figure 5

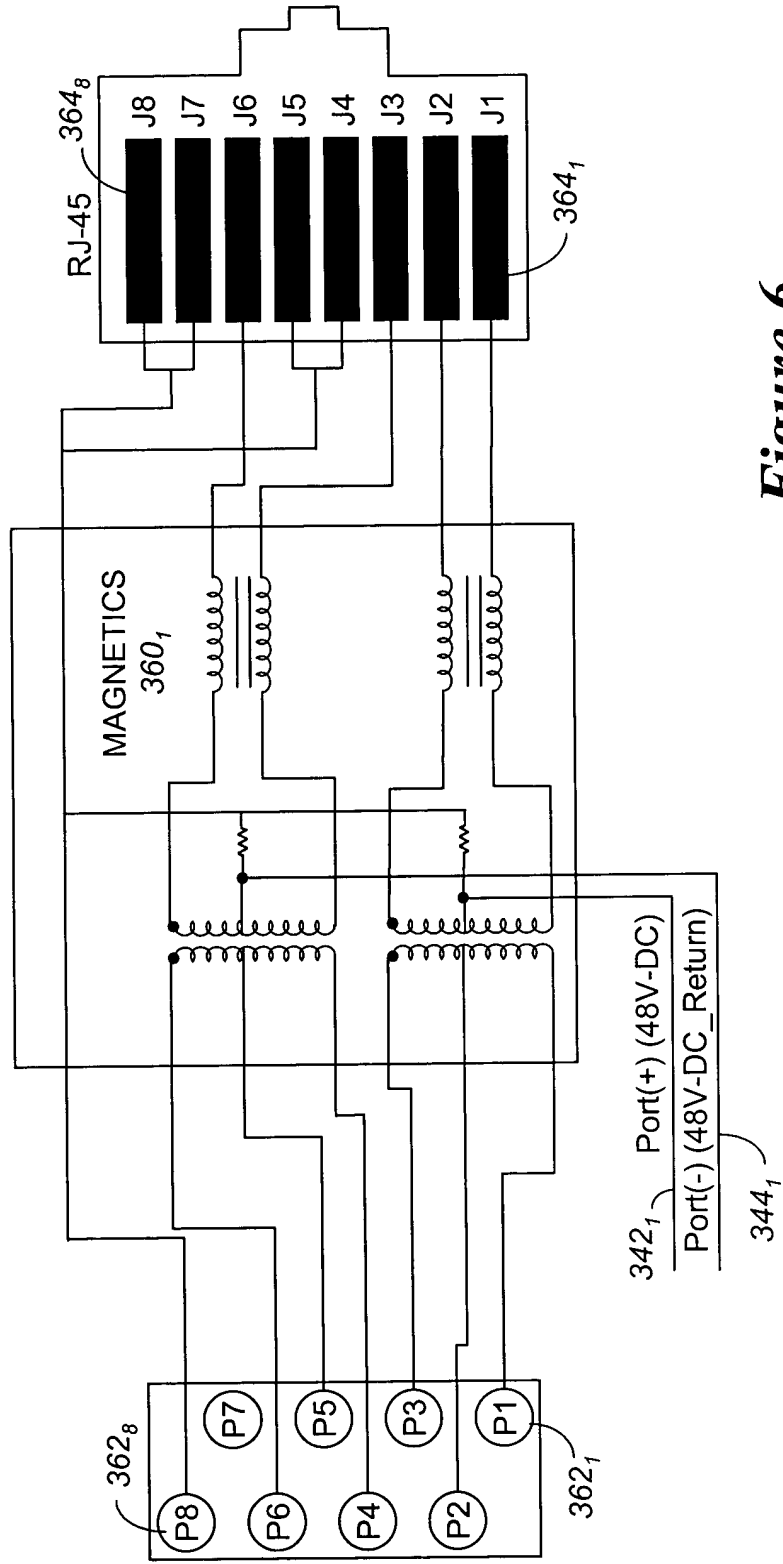


Figure 6

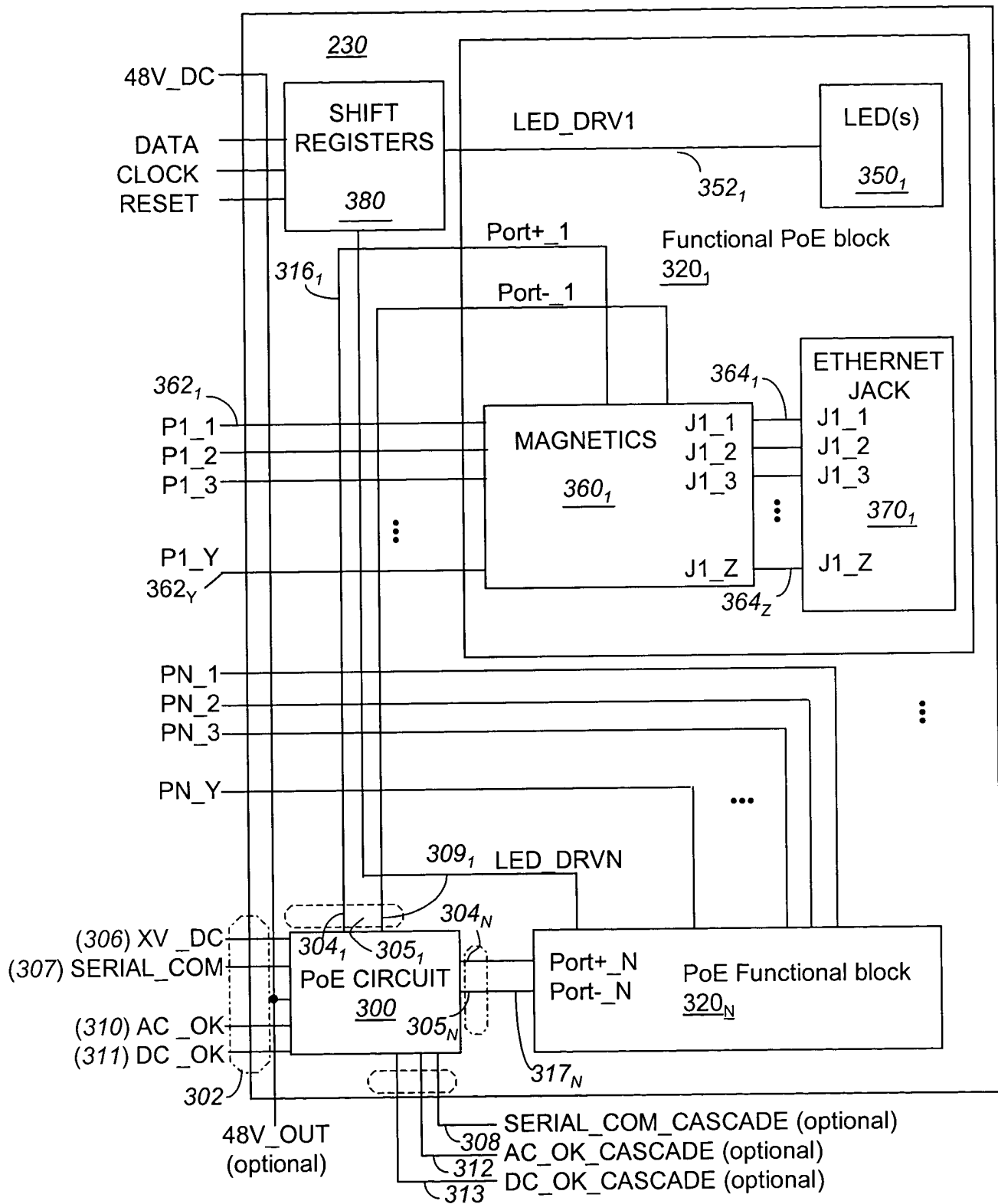


Figure 7

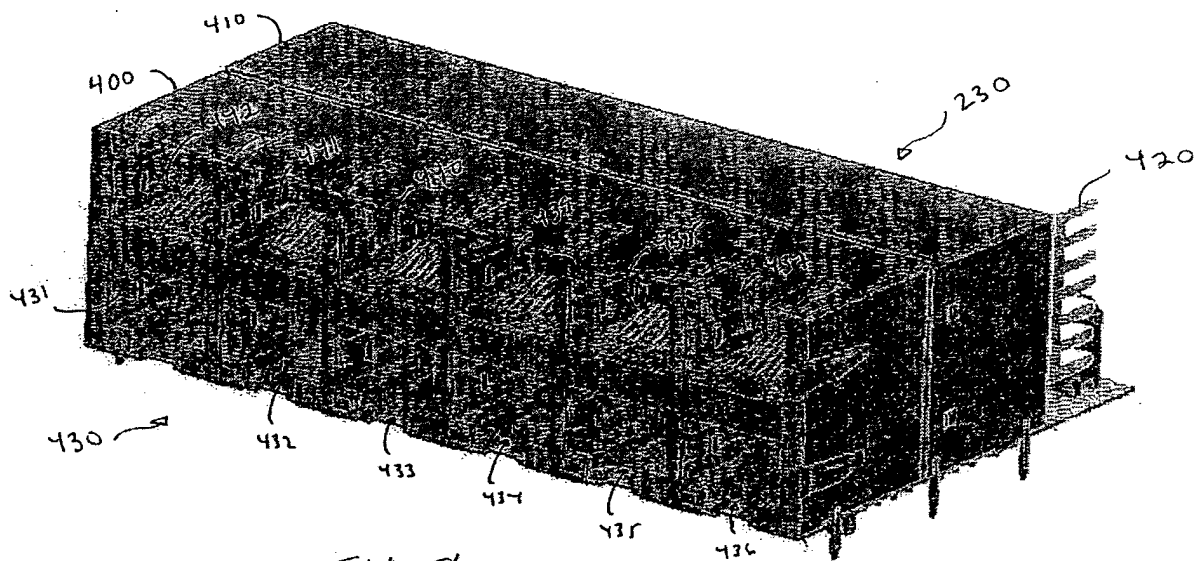
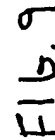


FIG. 8

PHY CHIP CONNECTION IS NOT SHOWN
PORT+ : 48V(TO RX)
PORT- : 48V RETURN PATH(ON TX)



FILED DATA OUT (FOR CASCADING)